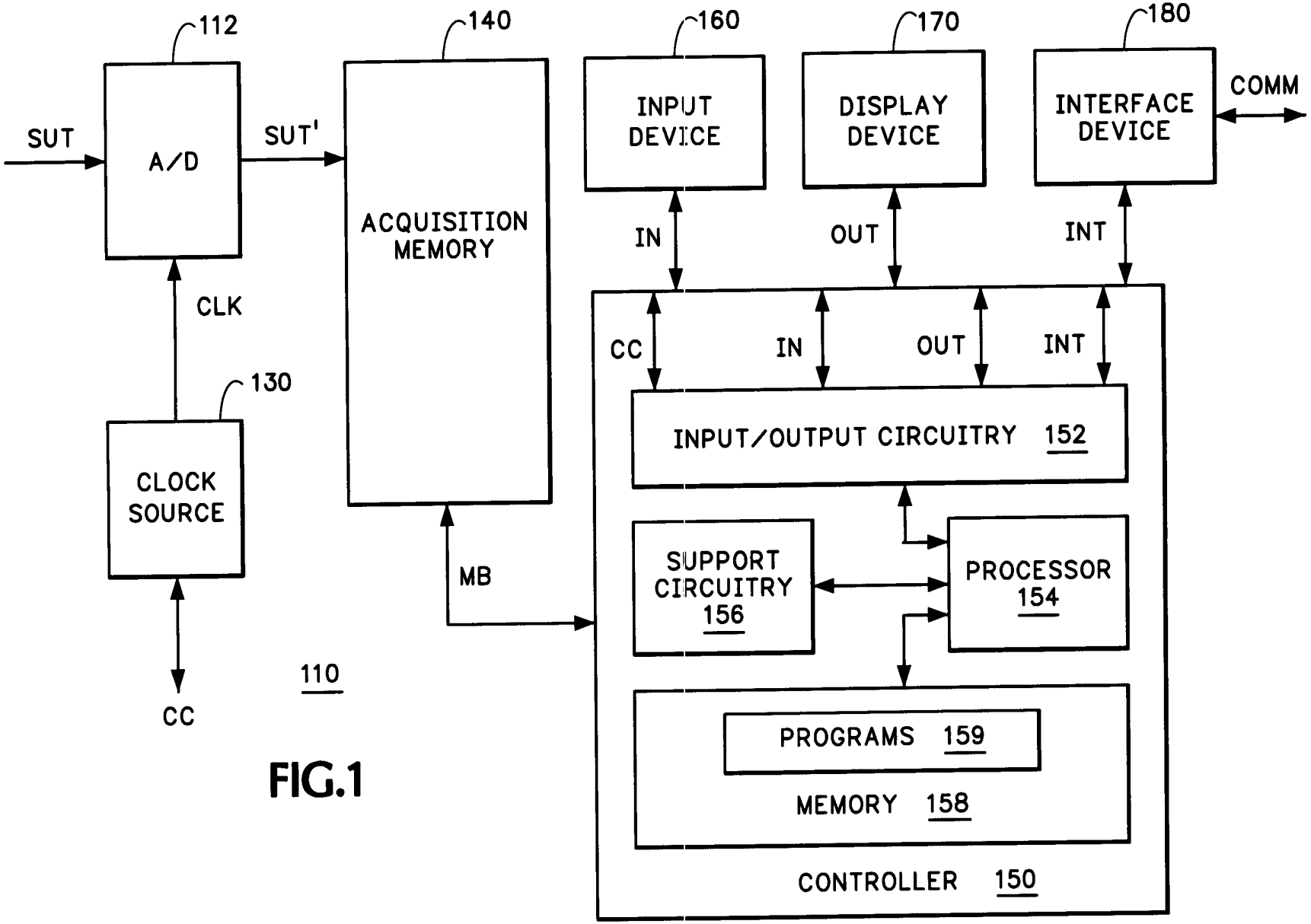
112
140
160
170
180
SUT
A/D
SUT'
ACQUISITION MEMORY
INPUT DEVICE
DISPLAY DEVICE
INTERFACE DEVICE
COMM
IN
OUT
INT
CLK
130
CC
IN
OUT
INT
INPUT/OUTPUT CIRCUITRY 152
CLOCK SOURCE
MB
SUPPORT CIRCUITRY 156
PROCESSOR 154
110
PROGRAMS 159
MEMORY 158
CC
CONTROLLER 150

FIG.1

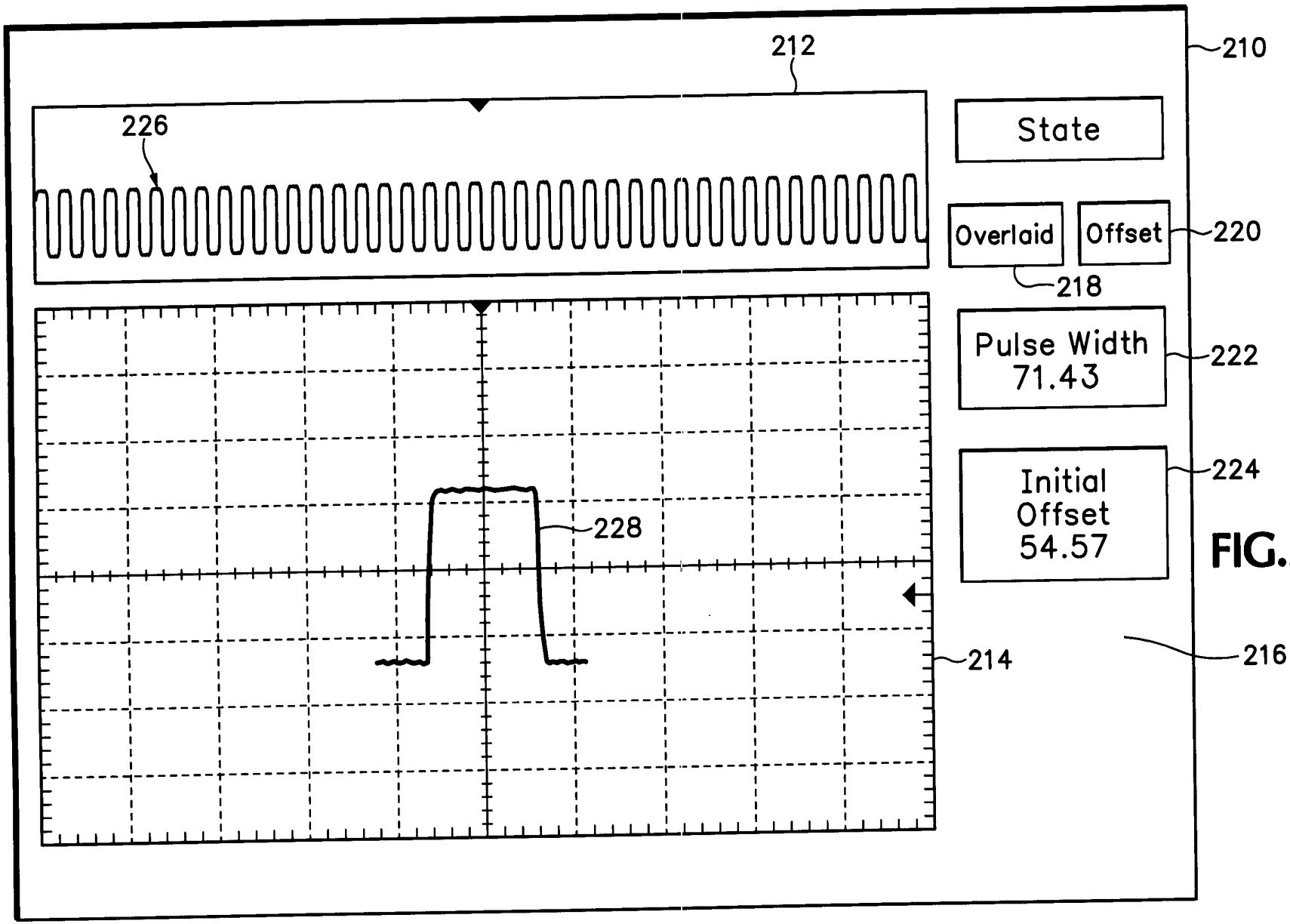
212
210
226
State
Overlaid
Offset
220
218
Pulse Width
71.43
222
Initial
Offset
54.57
224
228
214
216

FIG.2

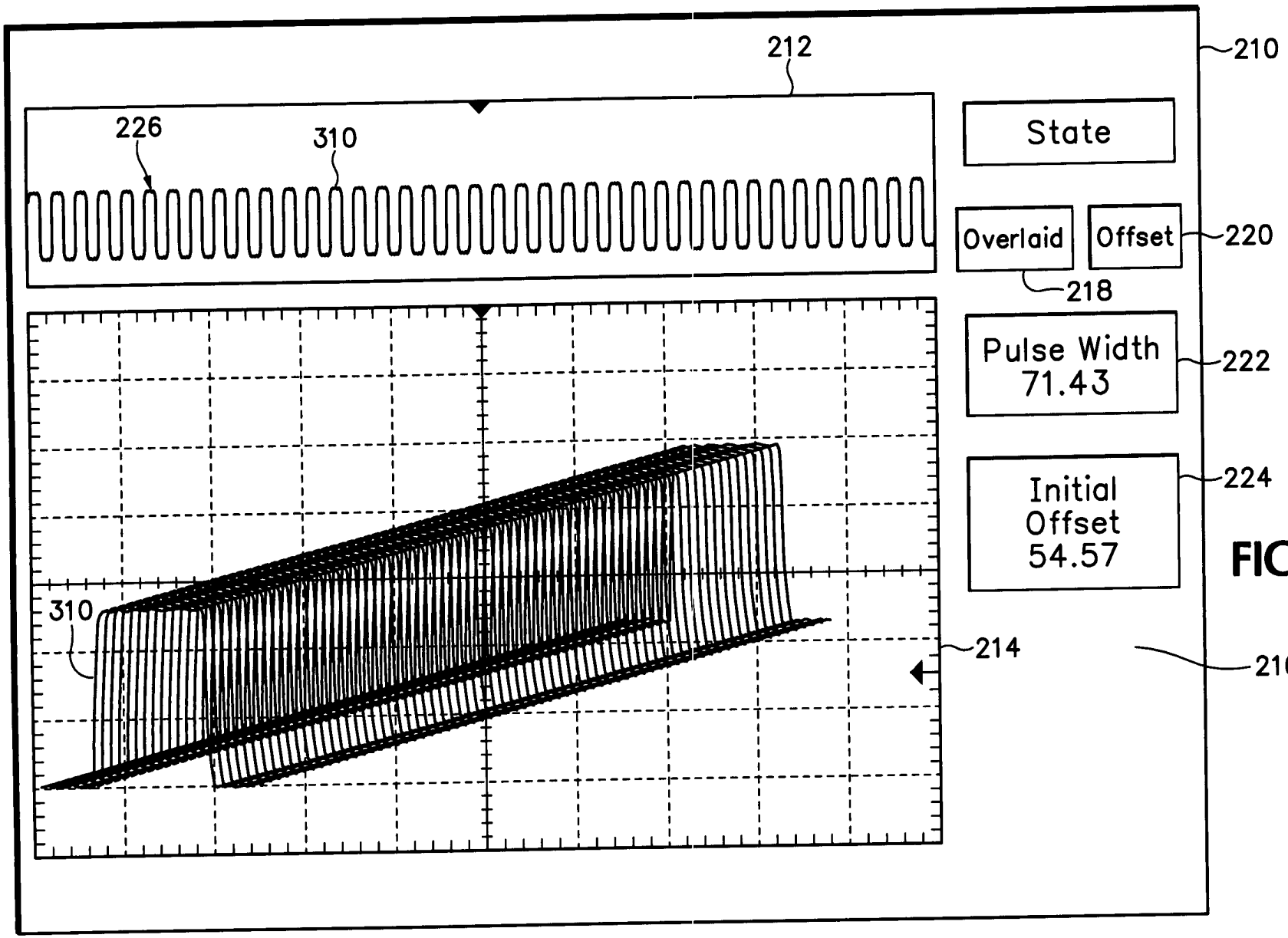
212
210
226
310
State
Overlaid
Offset
220
218
Pulse Width
71.43
222
Initial
Offset
54.57
224
310
214
216

FIG.3

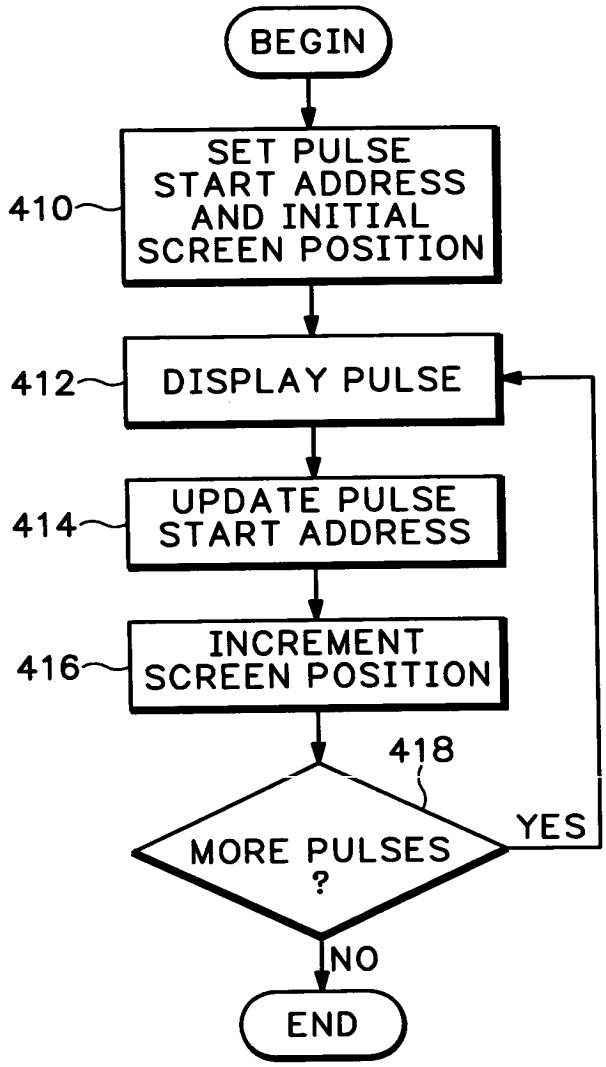
BEGIN
SET PULSE START ADDRESS AND INITIAL SCREEN POSITION
410
DISPLAY PULSE
412
UPDATE PULSE START ADDRESS
414
INCREMENT SCREEN POSITION
416
418
MORE PULSES ?
YES
NO
END

FIG.4